

### **REMARKS/ARGUMENTS**

Claims 2 and 3 were objected to. Claim 19 was objected to. Claims 1 to 7 were rejected under 35 USC §112, second paragraph. Claims 1 to 9, 13 to 15 and 19 were rejected under 35 USC §102(b) as being anticipated by Platteter et al. (U.S. 5,629,775). Claim 11 was rejected under 35 USC §103(a) as being unpatentable over Platteter et al. (U.S. 5,629,775) as applied to claim 8 and further in view of Okano (U.S. 2001/0011219). Claims 12, 16 and 17 were rejected under 35 USC §103(a) as being unpatentable over Platteter et al. (U.S. 5,629,775) in view of Goers et al. (U.S. 2002/0096942).

Claim 18 was objected to as being dependent upon a rejected base claim, but was indicated as allowable if rewritten in independent form.

Paragraphs [0034], [0042] and [0043] of the specification have been amended.

Claims 1, 2, 3 and 19 have been amended. Claim 18 has been cancelled without prejudice and will be presented in a continuation application.

Replacement Sheets are submitted herewith including Figs. 1 to 7.

Applicants respectfully request reconsideration of the application based on the amendments and the following remarks.

#### **Claim Objections**

Claims 2 and 3 were objected to. Claim 19 was objected to.

Claims 1, 2, 3 and 19 have been amended to provide proper antecedent basis and withdrawal of the objections to claims 2, 3 and 19 is respectfully requested.

#### **35 U.S.C. §112 Rejection, second paragraph**

Claims 1 to 7 were rejected under 35 USC §112, second paragraph.

Claim 1 has been amended to remove the term “or to be attached to.”

Withdrawal of the rejection under 35 USC §112, second paragraph, of claims 1 to 7 is respectfully requested.

35 U.S.C. §102(b) Rejection

Claims 1 to 9, 13 to 15 and 19 were rejected under 35 USC §102(b) as being anticipated by Platteter et al.

Platteter et al. discloses an electronic image processing apparatus having a plurality of resources wherein each of the resources includes an associated processor for storing data related to the operational timing of the associated resource. (See col. 2; lines 19 to 23). The SBC PWBA 22 includes memory 22A for storing data related to the operation and timing of the associated feeder or finisher and logic and circuitry 22B for interrogating each of the device PWBA's for the operational and timing data of the device PWBA. (See col. 4; lines 1 to 6).

Claim 1 recites a method for detecting a type of one of a plurality of devices attached to a graphics machine, each device being one of at least a first type, a second type, and a third type, the method comprising:

detecting at a controller whether a first of the plurality of devices attached to the machine is of the first type, the second type or the third type, the controller being capable of preadjusting the device as a function of the detection.

Claim 8 recites a graphics machine comprising:

a controller;

a first device connected to the controller, the first device being categorizable as one of at least a first type, a second type, and a third type, the controller detecting whether the first device is of the first type, the second type or the third type; and

a memory accessible by the controller, the memory storing information regarding the first type and the second type and the third type;

wherein the controller automatically adjusts the first device as a function of the information.

In order to anticipate a claim, the reference must teach every element of the claim. (MPEP 2131). Platteter et al. does not disclose "detecting at a controller whether a first of the plurality of devices attached to the machine is of the first type, the second type or the third type, the controller being capable of preadjusting the device as a function of the detection" as in claim 1. Platteter et al. does not disclose "the controller detecting whether the first device is of the first type, the second type or the third type" as in claim 8. In fact,

Platteter et al. teaches away from the present invention because only operational and timing data are detected by the PWBA. There is no evidence or support for the Examiner's assertion that detecting the type of device is inherent in the system for the graphics machine.

Detecting the operation of a device is different from detecting the type of device, as discussed in the present application at [0005] to [0010], for example. Since all elements of claims 1 and 8 are not taught or shown in Platteter et al., there is no anticipation under 35 U.S.C. §102(b).

Withdrawal of rejections under 35 U.S.C. §102(b) of claim 1 and its dependent claims and claim 8 and its dependent claims is respectfully requested.

### 35 U.S.C. §103(a) Rejection

Claim 11 was rejected under 35 USC §103(a) as being unpatentable over Platteter et al. as applied to claim 8 and further in view of Okano.

Claim 11 recites "wherein the information is stored as a table."

Platteter et al. does not disclose "information is stored as a table" as claimed. There is no motivation to one skilled in the art to modify Platteter et al. in view of Okano. Okano is non-analogous art and not in the field of invention related to post-press gathering machines for printed products, such as newspaper and magazines.

Withdrawal of rejection under 35 U.S.C. §103(a) of claim 11 is respectfully requested.

Claims 12, 16 and 17 were rejected under 35 USC §103(a) as being unpatentable over Platteter et al. in view of Goers et al.

Claim 12 recites "wherein the first device is connected to the controller via an electrical plug, a fixed transmission line or a wireless connection."

Claim 16 recites "wherein the type identifier is a plug having an input power pin and at least one other pin, the first type or second type being identified by a connection between the power pin and the other pin."

Claim 17 recites "wherein the input power pin and the other pin are separated by a resistor."

As admitted in the Office Action, Platteter et al. does not disclose how the devices are connected to the machine. Therefore, there is no motivation to one skilled in the art to modify Platteter et al. in view of Goers et al. because Goers et al. teaches managing electrical interfaces to avoid damage to the unit to be inserted and not to control the device. (See, for example paragraph [0009]). Furthermore, in Goers et al., there is no evidence that Pin 264 is regarded as an input power pin, as Pin 264 is described as a control input. (See paragraphs [0032] and [0050], for example). Platteter et al. in view of Goers et al. does not teach “wherein the first device is connected to the controller” as recited in claim 12. Platteter et al. in view of Goers et al. does not teach “wherein the type identifier is a plug having an input power pin” as recited in claim 16. Platteter et al. in view of Goers et al. does not teach “wherein the input power pin and the other pin are separated by a resistor” as recited in claim 17.

Withdrawal of the rejection under 35 U.S.C. 103(a) to claims 12, 16 and 17 is respectfully requested.

#### Allowable Subject Matter

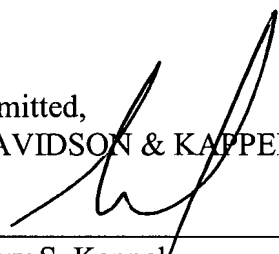
Claim 18 was objected to as being dependent upon a rejected base claim, but indicated as allowable if rewritten in independent form.

Claim 18 has been cancelled in favor of a continuation application.

**CONCLUSION**

The present application is respectfully submitted as being in condition for allowance and applicants respectfully request such action.

Respectfully submitted,  
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